

AMENDMENTS TO THE CLAIMS

Claim 1 (Currently Amended) A video composition circuit ~~for receiving plural pieces of video data which are successively inputted in serial order, performing a predetermined video processing for predetermined video data, and combining plural pieces of video data to output composite data, comprising:~~

 a video processing unit (i) receiving to which plural pieces of video data are successively input-inputted in serial order, (ii) said video processing unit performing a predetermined video processing to the received pieces of for the inputted video data, and (iii) outputting the pieces of processed video data having the predetermined video processing performed thereon;

 a video data composition unit including an α -blending circuit for outputting α -blended video data ~~combining the plural pieces of video data outputted from the video processing unit to output composite data; and~~

an internal α -data storage unit for storing the α -blended holding the video data output- ~~outputted from the video data composition unit~~[[;]],

wherein the video processing unit is operable to (i) receive the pieces of video data from an external storage unit, (ii) receive, from the internal data storage unit, the α -blended video data previously stored in the internal data storage unit, and (iii) perform the predetermined video processing on the received video data and the α -blended video data, and

wherein the said video data composition unit α -blending being a circuit having an α -blending function, and combining the combines (i) the α -blended video data previously stored by the internal data storage unit video data read from the data storage unit and (ii) the video data

~~currently output-outputted~~ from the video processing unit, ~~as well as and combines-combining~~ the plural pieces of video data ~~output-outputted~~ from the video processing unit, thereby to ~~perform-performing~~ vertical filtering to the video data successively ~~input-inputted~~ in serial order and to the ~~α -blended~~ α -blended video data previously stored in the internal data storage unit.

Claim 2 (Currently Amended) A video composition circuit as defined in Claim 1, wherein ~~said the~~ video processing unit, ~~said the~~ internal data storage unit, and ~~said the~~ video data composition unit are constituted ~~on the same~~ by a single chip.

Claim 3 (Currently Amended) A video composition circuit as defined in Claim 1, wherein the plural pieces of video data successively ~~input-inputted~~ in serial order are main video, sub-video, and OSD video ~~which that~~ is additional information to be displayed simultaneously with the main video and the sub-video.

Claim 4 (Currently Amended) A video composition circuit as defined in Claim 3 further, including

wherein the video composition circuit further includes an ~~the~~ external storage unit ~~for~~ holding the plural pieces of video data ~~that are successively inputted in serial order~~, ~~said external storage unit being that is disposed~~ placed outside the single chip; ~~and, and~~

wherein the ~~said~~ video data composition unit reads-reading the pieces of video data ~~output-outputted~~ from the external storage unit and the α -blended video data ~~which is~~ stored in the internal data storage unit ~~in the chip~~, and subjects the read pieces of video data and the α -

~~blended video data to~~ ~~subjecting these data to~~ α -blending again.

Claim 5 (Cancelled)

Claim 6 (Currently Amended) A video composition circuit as defined in Claim 1 wherein ~~said the~~ video data composition unit writes ~~the video data which is~~ obtained as a result of combining the α -blended video data previously stored by the internal data storage unit ~~video data read from the data storage unit~~ and the video data currently output ~~outputted~~ from the video processing unit, over ~~the video data which has previously been~~ stored in the internal data storage unit.